

FIG. 1

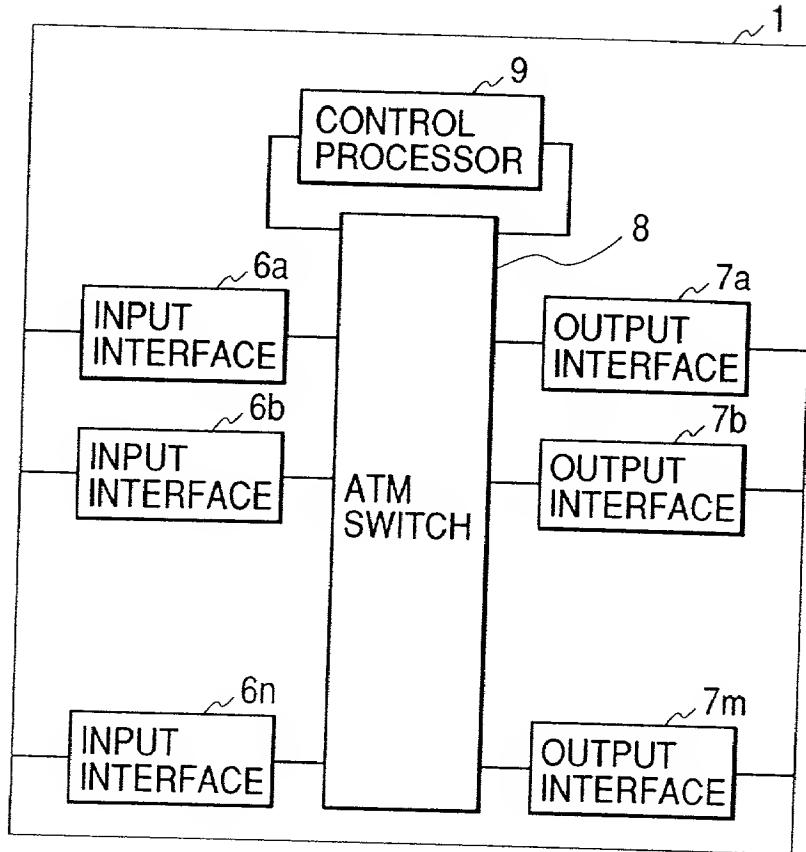


FIG. 2

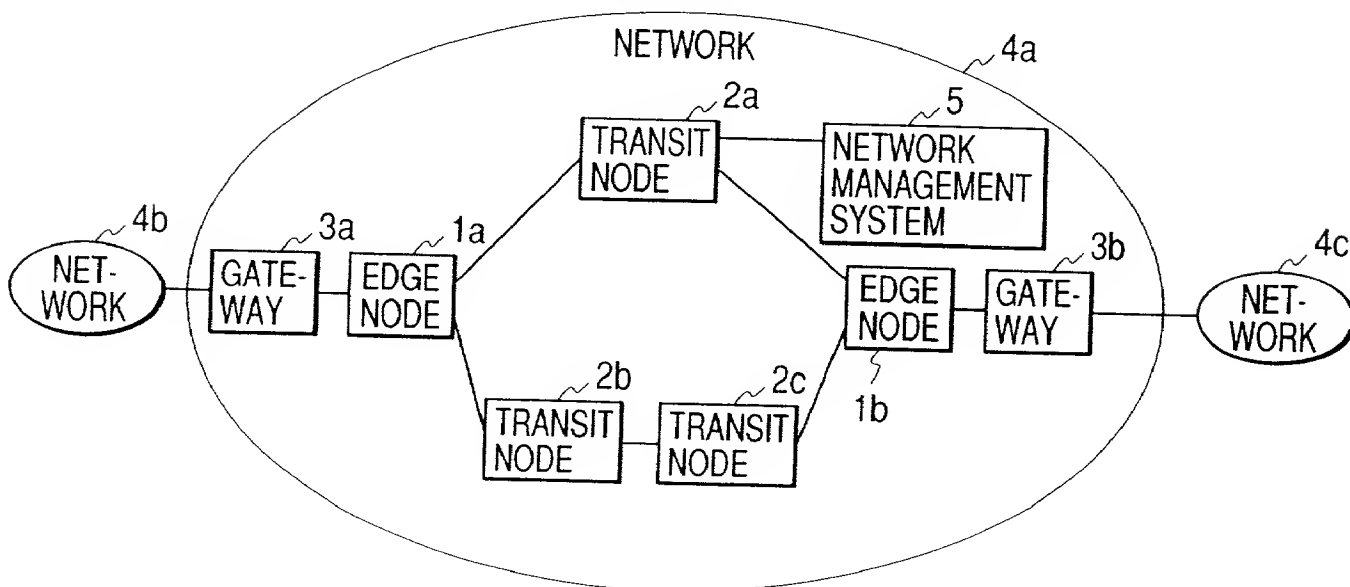


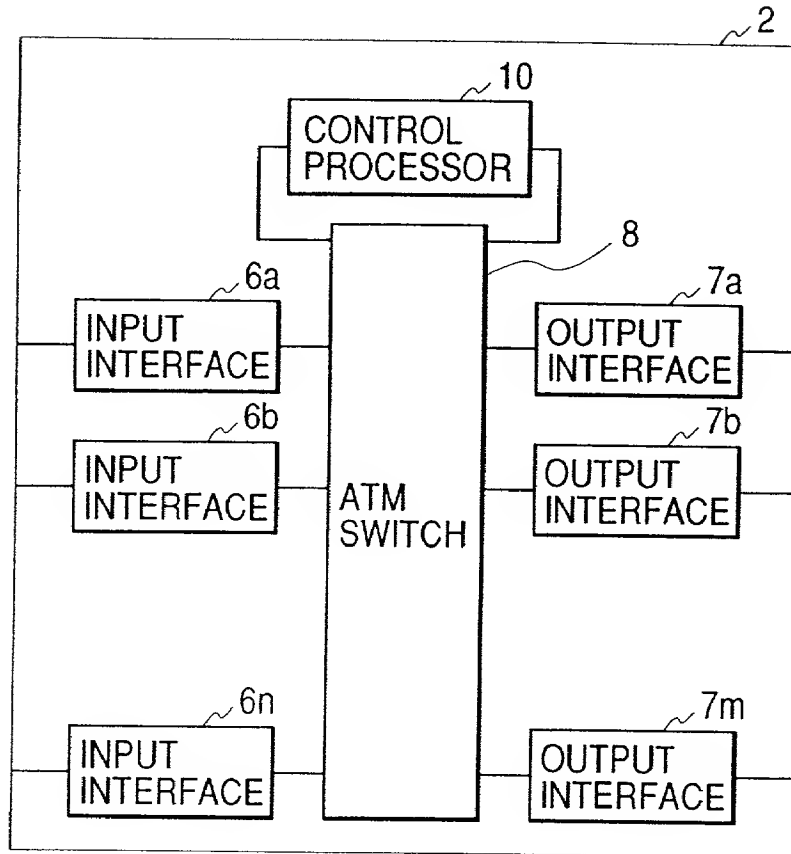
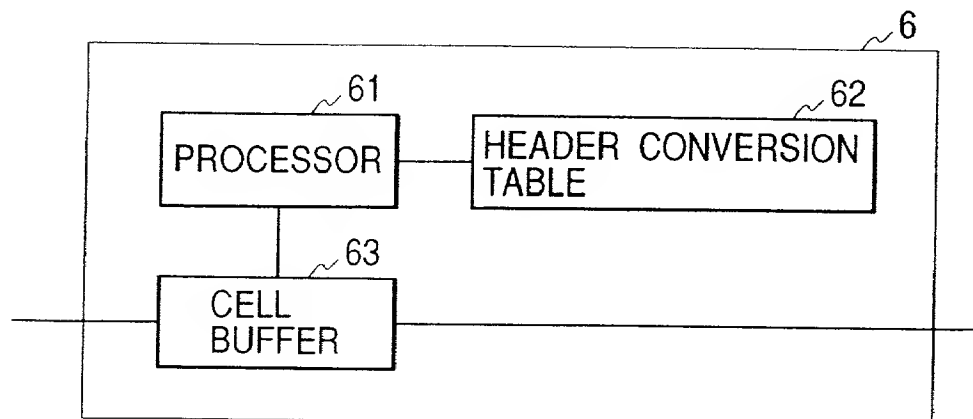
FIG. 3*FIG. 4*

FIG. 5

621 INPUT VPI	622 INPUT VCI	623 VP SWITCH FLAG	624 OUTPUT INTERFACE NUMBER	625 OUTPUT VPI	626 OUTPUT VCI	627 NUMBER OF ARRIVAL CELLS

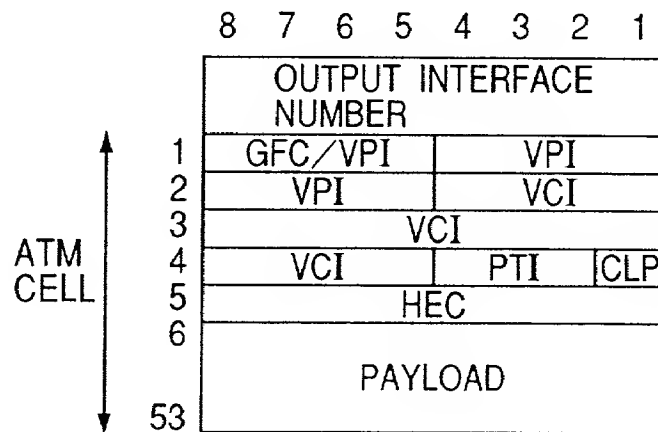
FIG. 6

FIG. 7

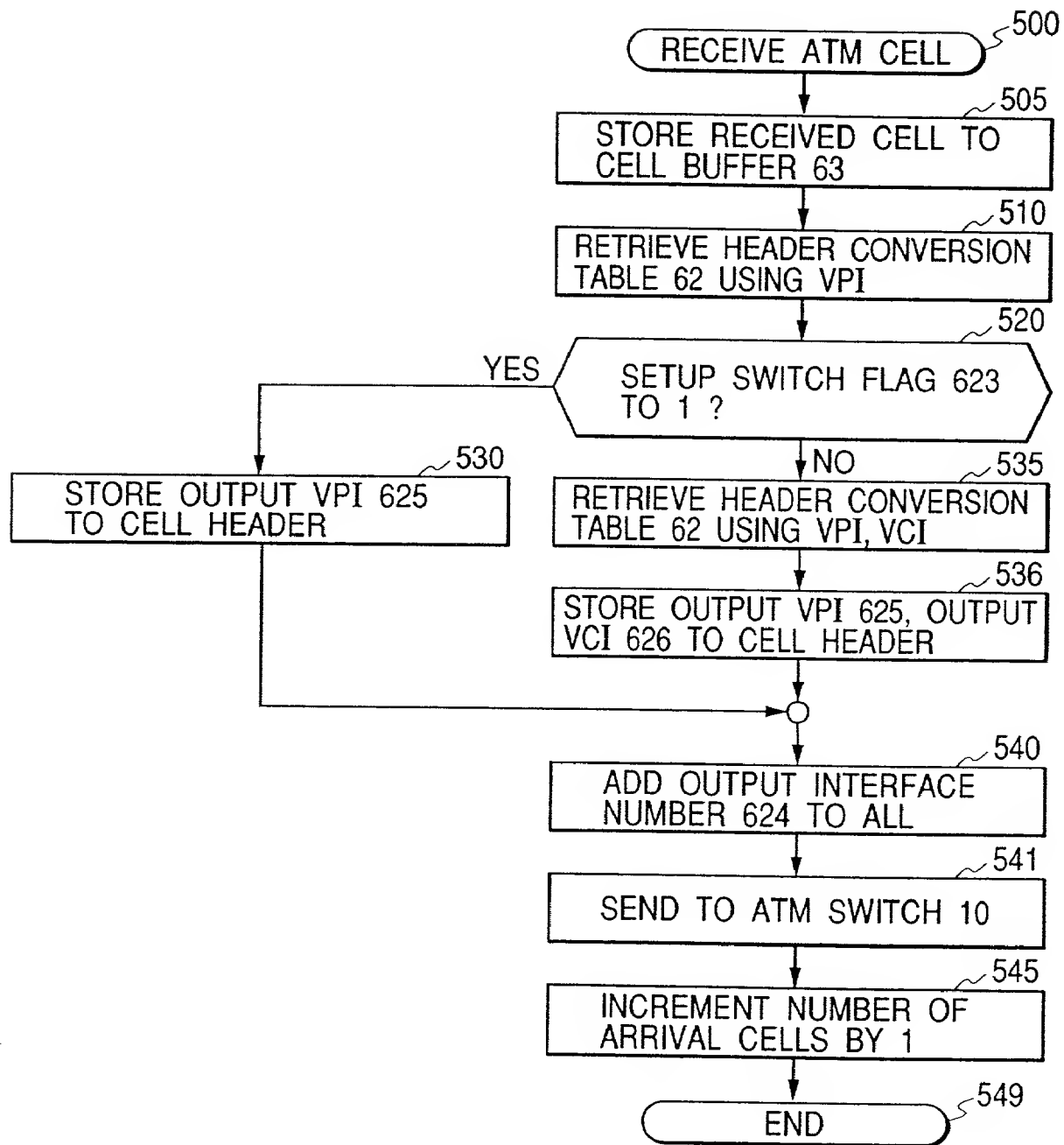


FIG. 8

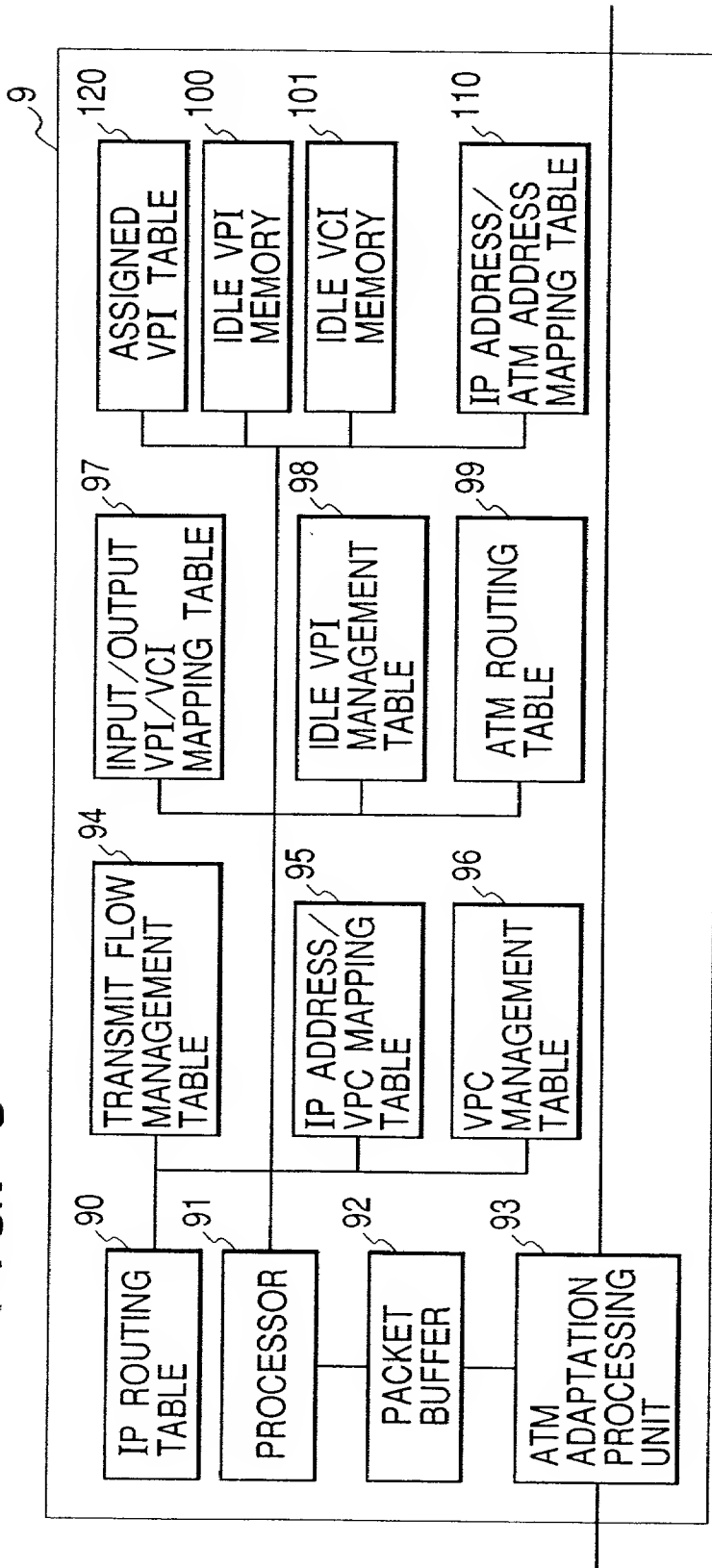


FIG. 9

901	902	903	904	905
DESTINATION NETWORK IP ADDRESS	MASK	METRIC	NEXT HOP NODE IP ADDRESS	NEXT HOP NODE ATM ADDRESS

FIG. 10

941 942 943 944 945 946 947 948 94

DESTINATION IP ADDRESS	SOURCE IP ADDRESS	PORT NUMBER	VPC IDENTIFIER	OUTPUT INTERFACE NUMBER	OUTPUT VPI	OUTPUT VCI	STATUS

FIG. 11

951 952 956 953 954 955 95

DESTINATION IP ADDRESS	PORT NUMBER	MASK	VPC IDENTIFIER	OUTPUT INTERFACE NUMBER	OUTPUT VPI

FIG. 12

961 ⌞	962 ⌞	963 ⌞	964 ⌞	965 ⌞	966 ⌞	967 ⌞	96
ATM ADDRESS	VPC IDENTIFIER	OUTPUT INTERFACE NUMBER	OUTPUT VPI	NUMBER OF IDLE VCI	DEFAULT VCI	IDLE VCI POINTER	

FIG. 13

971 ⌞	972 ⌞	973 ⌞	974 ⌞	975 ⌞	976 ⌞	977 ⌞	97
RECEIVED VPI	RECEIVED VCI	INPUT INTERFACE NUMBER	INPUT VPI	INPUT VCI	STATUS	FLOW POINTER	

FIG. 14

981 OUTPUT INTERFACE NUMBER	982 IDLE VPI POINTER

FIG. 15

991 DESTINATION ATM ADDRESS	992 NEXT HOP ATM ADDRESS	993 OUTPUT INTERFACE NUMBER	994 OUTPUT VPI	995 OUTPUT VCI

FIG. 16

111 IP ADDRESS	112 ATM ADDRESS

FIG. 17

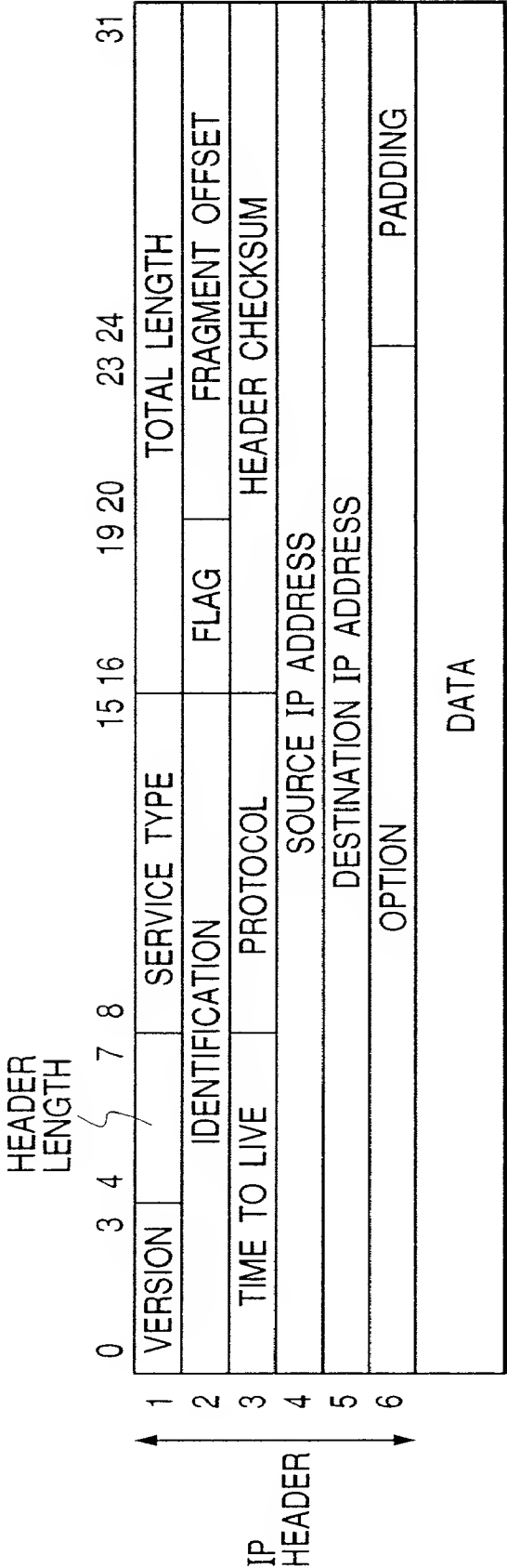


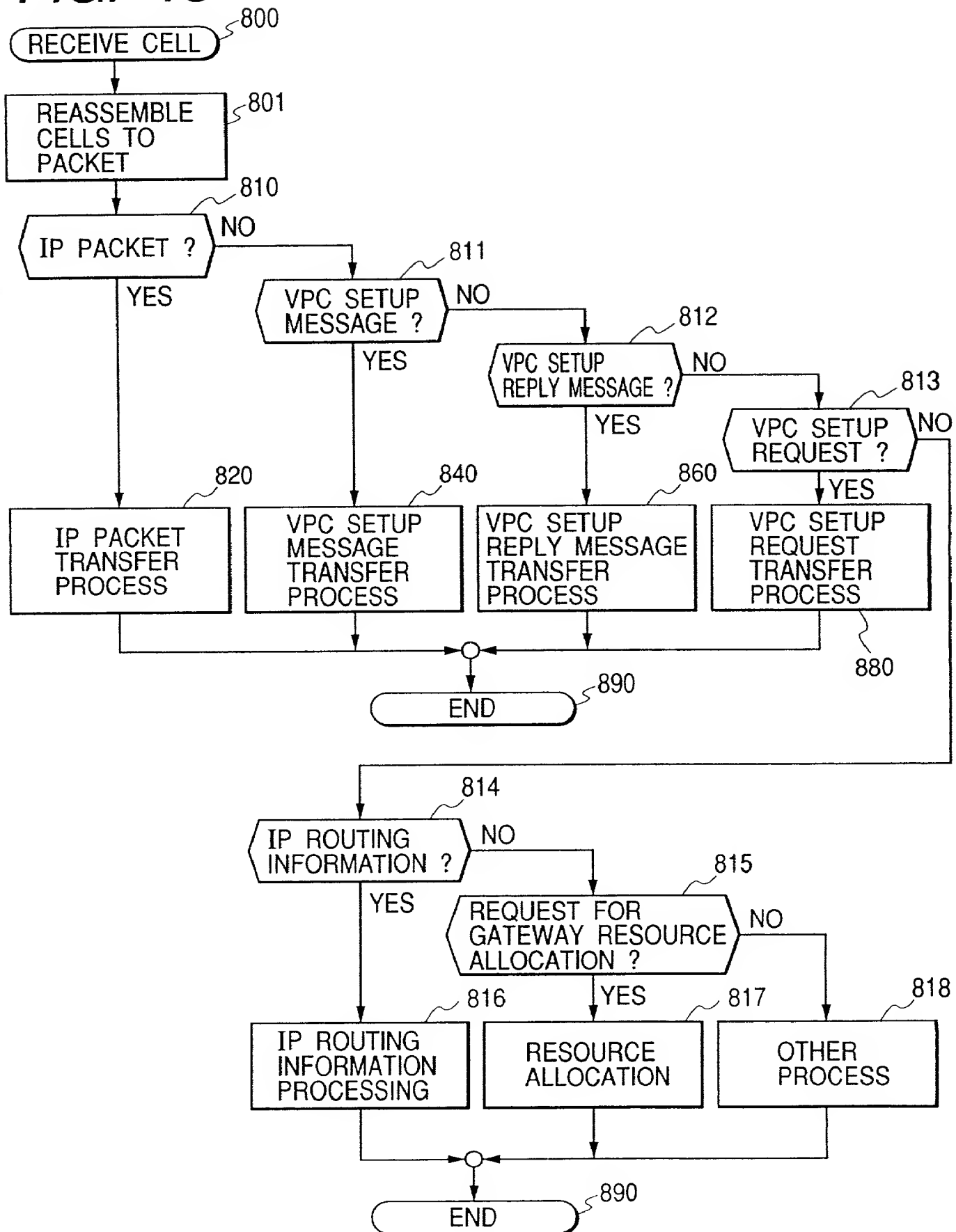
FIG. 18

FIG. 19

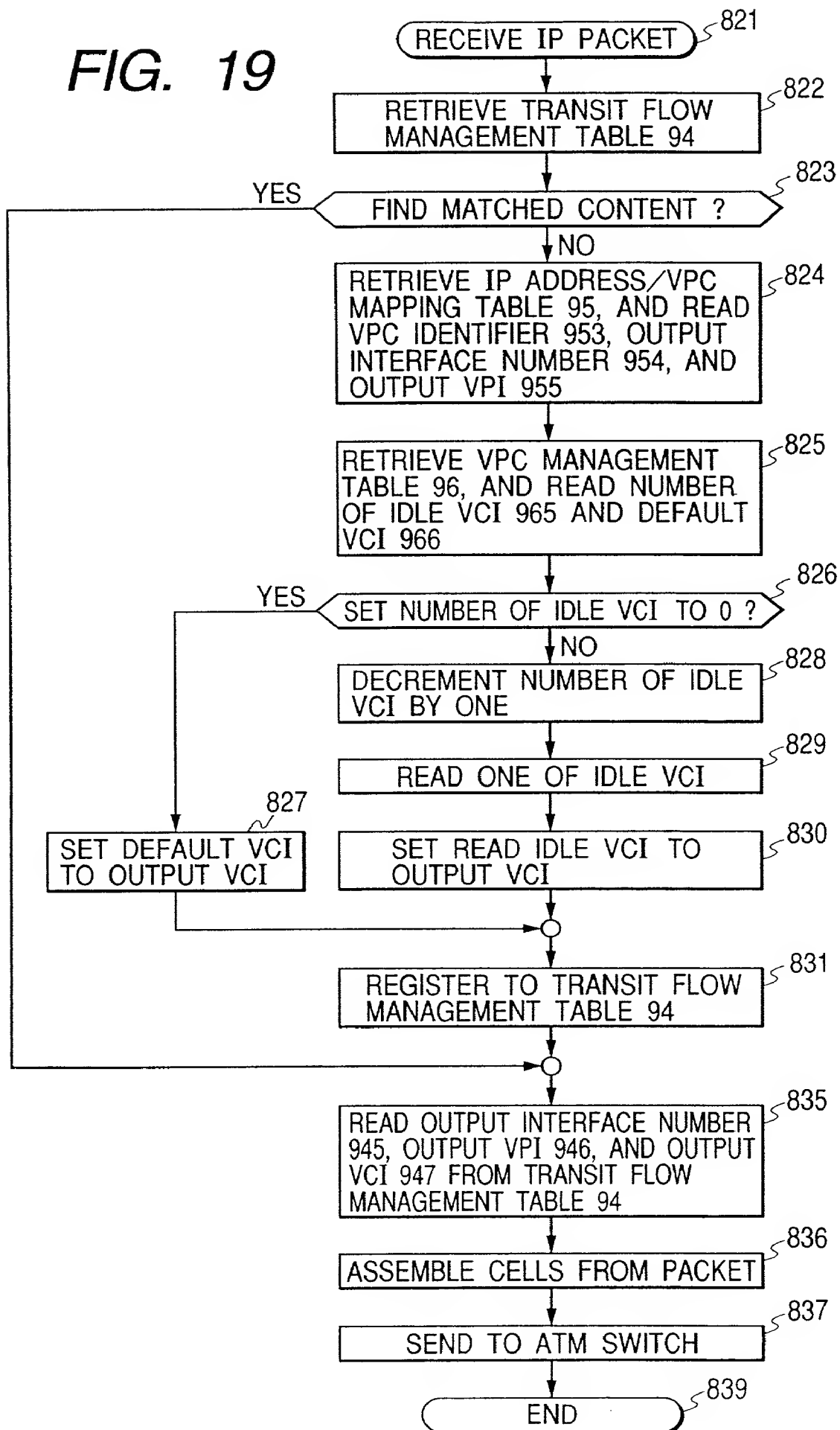


FIG. 20

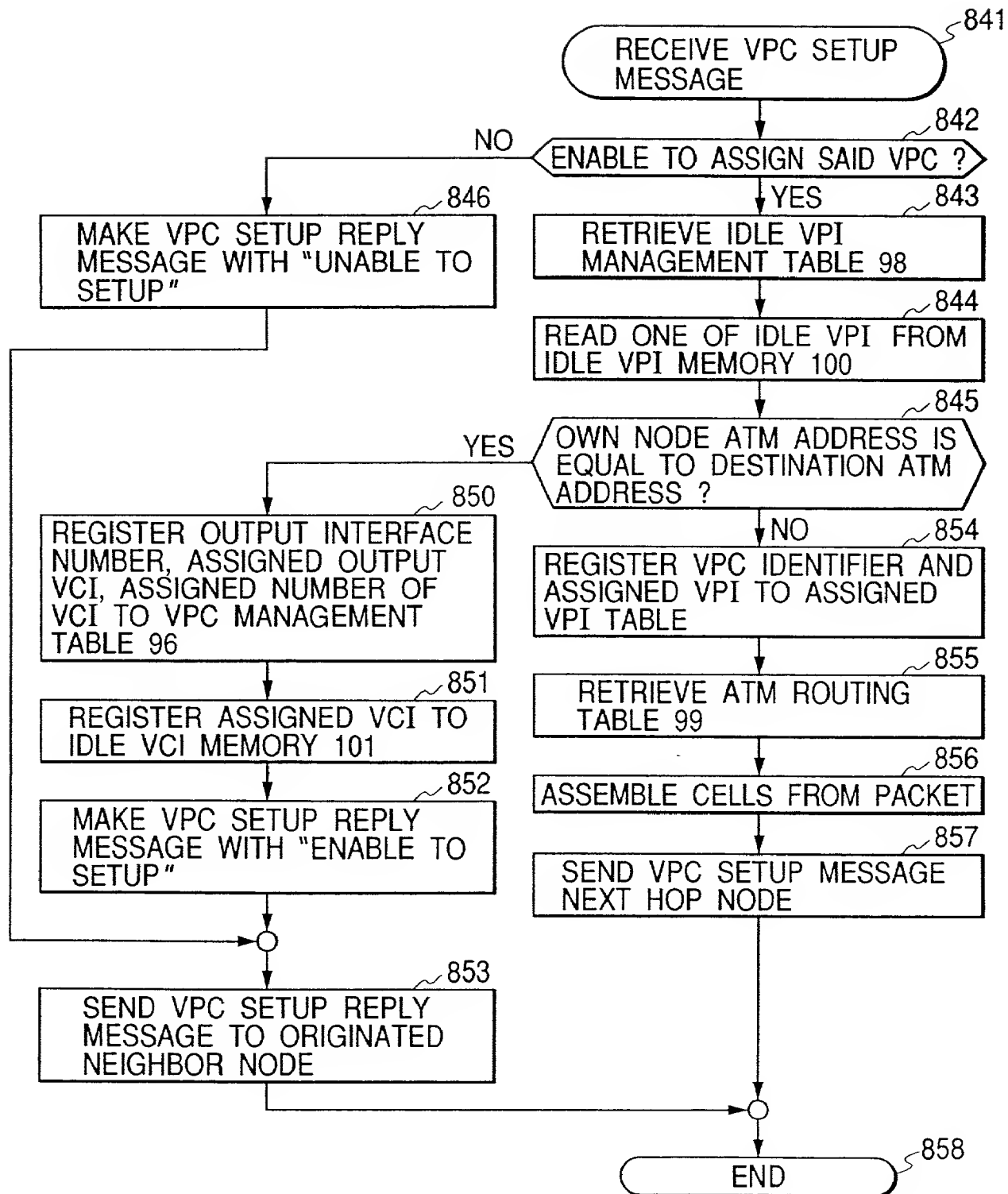


FIG. 21

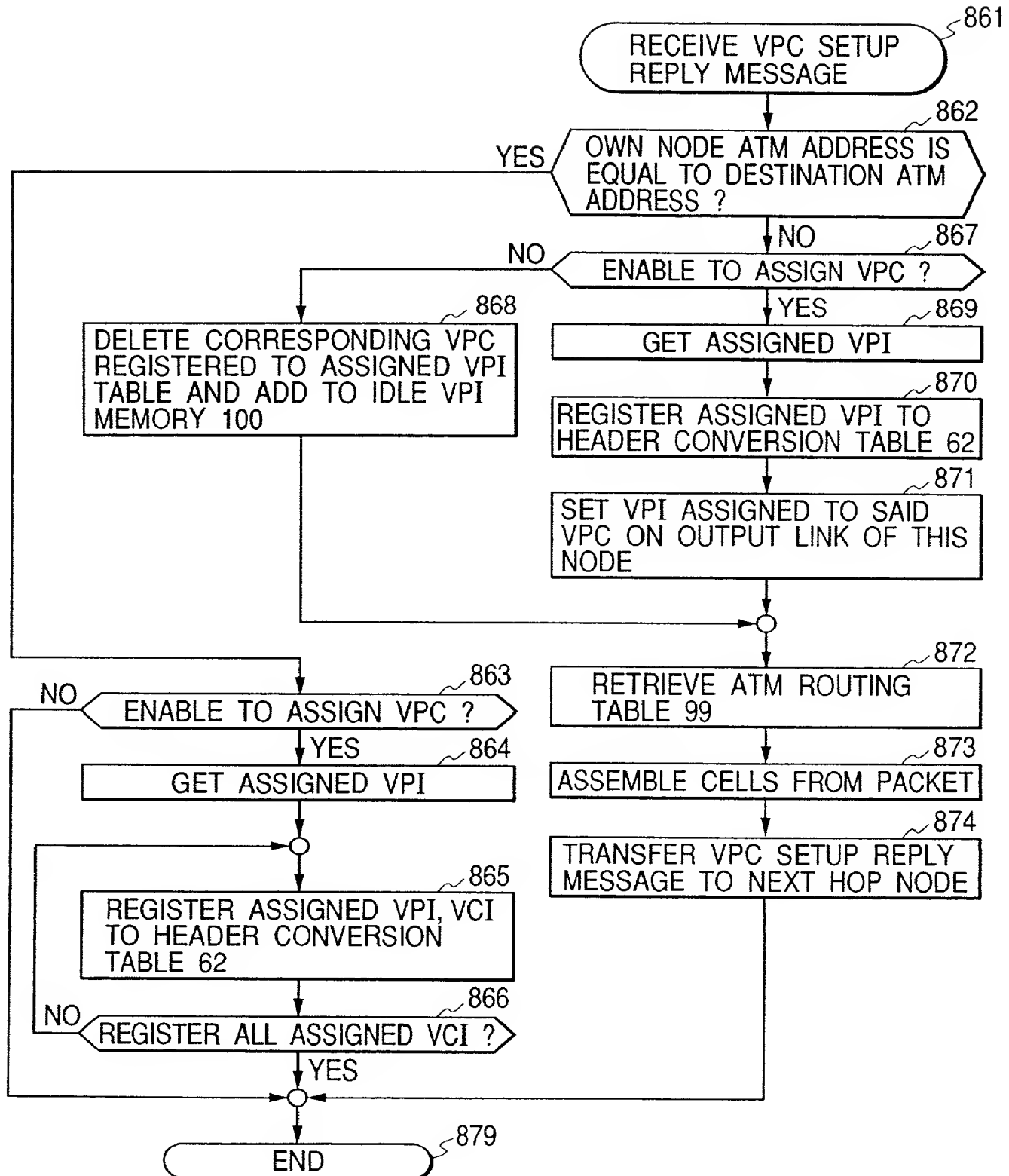


FIG. 22

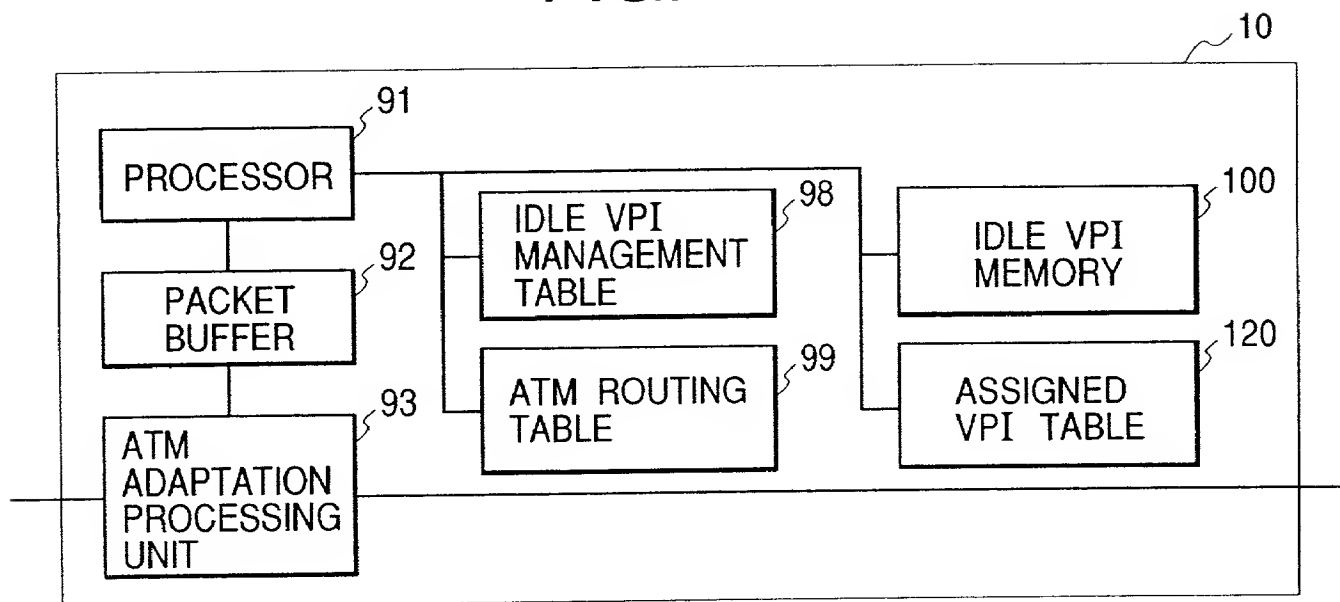


FIG. 23

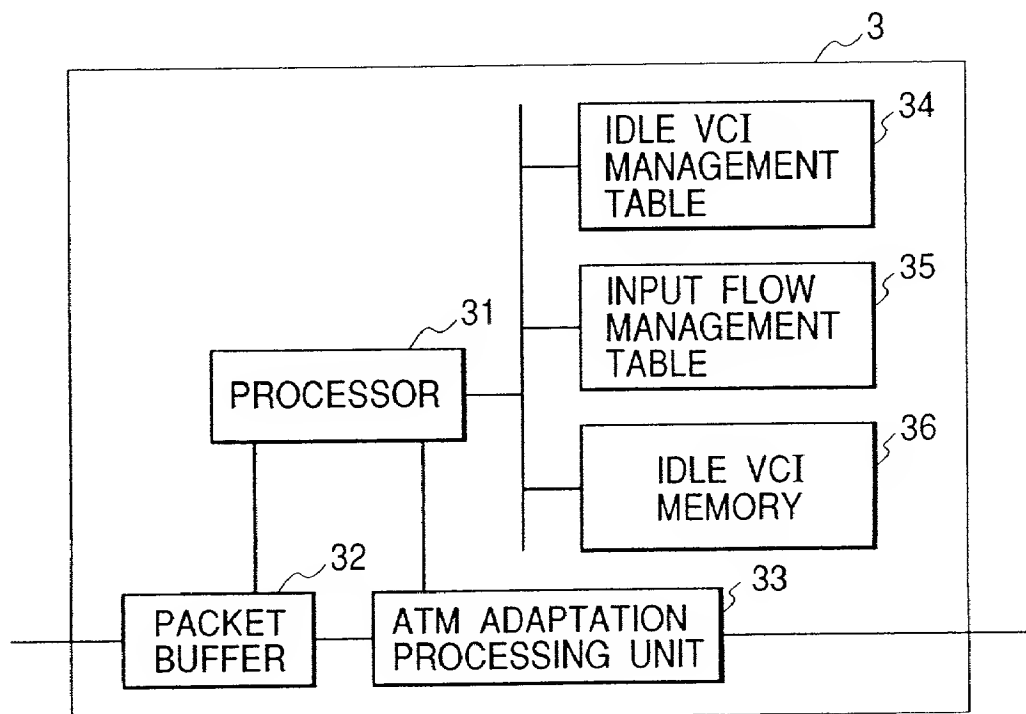


FIG. 24

341 VPI	342 NUMBER OF IDLE VCIS	343 DEFAULT VCI	344 IDLE VCI POINTER

FIG. 25

351 DESTINATION IP ADDRESS	352 SOURCE IP ADDRESS	353 PORT NUMBER	354 OUTPUT VPI	355 OUTPUT VCI	356 STATUS

FIG. 26

